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Bib Data Sheet

CONFIRMATION NO. 1890

SERIAL NUMBER 10/770,479	FILING DATE 02/04/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. NITT.0189
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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of 10/083,341 02/27/2002 PAT 6,708,322

VERIFIED

** FOREIGN APPLICATIONS *****

JAPAN 2001-170636 06/06/2001

VERIFIED

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/01/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature	Initials				

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TITLE

Integrated circuit, integrated circuit design method and hardware description generation method to generate hardware behavior description of integrated circuit

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